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Vishay Siliconix

Automotive P-Channel 60 V (D-S) 175 °C MOSFET

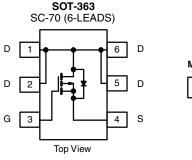
PRODUCT SUMMARY				
V _{DS} (V)	- 60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.290			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.395			
I _D (A)	- 1.6			
Configuration	Single			

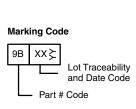
FEATURES

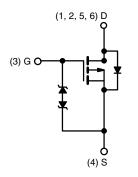
- Halogen-free According to IEC 61249-2-21 **Definition**
- TrenchFET® Power MOSFET
- AEC-Q101 Qualified^d
- \bullet 100 % R_{g} and UIS Tested
- Typical ESD Protection: 800 V
- Compliant to RoHS Directive 2002/95/EC











P-Channel MOSFET

ORDERING INFORMATION	
Package	SC-70
Lead (Pb)-free and Halogen-free	SQ1421EEH-T1-GE3

ABSOLUTE MAXIMUM RATING	S (T _C = 25 °C, unles	s otherwise noted	(k	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	- 60	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	T _C = 25 °C ^a		- 1.6	
Continuous Drain Current	T _C = 125 °C	I _D	- 1.4	
Continuous Source Current (Diode Conduction) ^a		Is	- 1.6	Α
Pulsed Drain Current ^b		I _{DM}	- 6.7	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	- 8	
Single Pulse Avalanche Energy	L = U. I IIII	E _{AS}	3.2	mJ
Maximum Power Dissipation ^b	T _C = 25 °C	Р	3.3	W
iviaximum Fower Dissipation	T _C = 125 °C	P_{D}	1.1	VV
Operating Junction and Storage Temperatu	re Range	T _J , T _{stg}	- 55 to + 175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient P	PCB Mount ^c	R_{thJA}	125	°C/W	
Junction-to-Foot (Drain)		R_{thJF}	45	C/VV	

- a. Package limited.
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %. c. When mounted on 1" square PCB (FR-4 material).
- d. Parametric verification ongoing.



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static	•	1			·	·		
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA		- 60		-	V	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	V _{GS} , I _D = - 250 μA	- 1.5	- 2.0	- 2.5	V	
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		-	± 5	μΑ	
Gate-Source Leakage		V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 5	mA	
		$V_{GS} = 0 V$	V _{DS} = - 60 V	-	-	- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = - 60 V, T _J = 125 °C	-	-	- 50	μΑ	
		V _{GS} = 0 V	V _{DS} = - 60 V, T _J = 175 °C	-	-	- 150		
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	$V_{DS} \le -5 V$	- 5	-	-	Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 10 V	I _D = - 2 A	-	0.230	0.290	Ω	
	В	V _{GS} = - 10 V	I _D = - 2 A, T _J = 125 °C	-		0.470		
	R _{DS(on)}	V _{GS} = - 10 V	I _D = - 2 A, T _J = 175 °C	-		0.566		
		V _{GS} = - 4.5 V	I _D = - 1 A	-	0.305	0.395		
Forward Transconductanceb	9 _{fs}	V _{DS} =	- 10 V, I _D = - 1.5 A	-	3	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}			-	284	355		
Output Capacitance	C _{oss}	$V_{GS} = 0 V$	V _{GS} = 0 V V _{DS} = - 25 V, f = 1 MHz		36	45	pF	
Reverse Transfer Capacitance	C _{rss}			-	28	35		
Total Gate Charge ^c	Qg			-	3.6	5.4		
Gate-Source Charge ^c	Q _{gs}	V _{GS} = - 4.5 V	$V_{GS} = -4.5 \text{ V}$ $V_{DS} = -30 \text{ V}, I_{D} = -1 \text{ A}$		1.2	-	nC	
Gate-Drain Charge ^c	Q_{gd}			-	1.7	-		
Gate Resistance	Rg	f = 1 MHz		3.1	6.05	9	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	44	66		
Rise Time ^c	t _r	$V_{DD} = -30 \text{ V}, R_L = 30 \Omega$ $I_D \cong -1 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		-	25	38	ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	13	20		
Fall Time ^c	t _f			-	9	14		
Source-Drain Diode Ratings and Chara	acteristics ^b							
Pulsed Current ^a	I _{SM}			ı	=	- 6.7	Α	
Forward Voltage	V _{SD}	I _F = -	- 0.5 A, V _{GS} = 0 V	-	- 0.8	- 1.2	V	

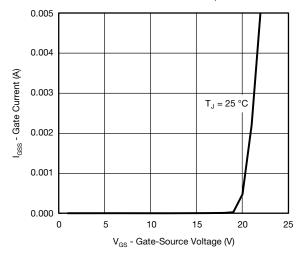
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

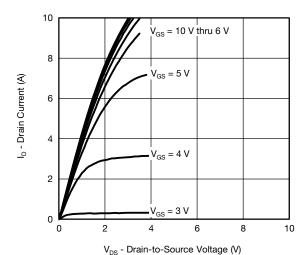
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



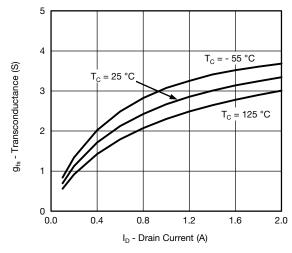
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



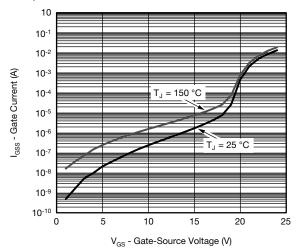
Gate Current vs. Gate-Source Voltage



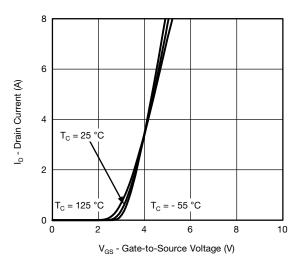
Output Characteristics



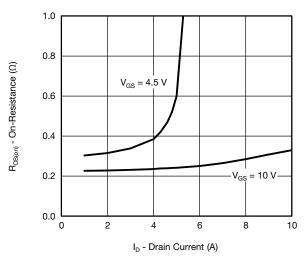
Transconductance



Gate Current vs. Gate-Source Voltage



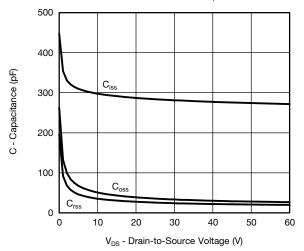
Transfer Characteristics



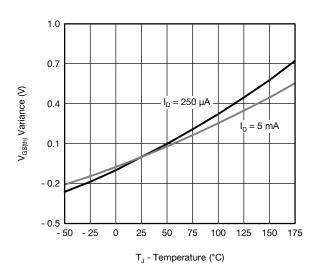
On-Resistance vs. Drain Current



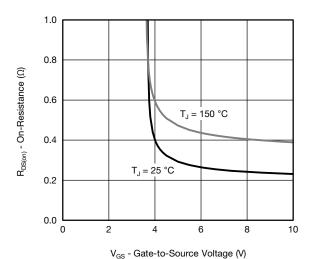
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



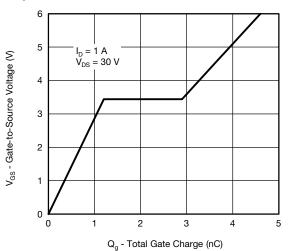
Capacitance



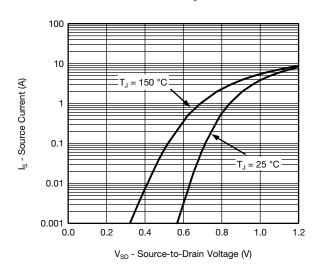
Threshold Voltage



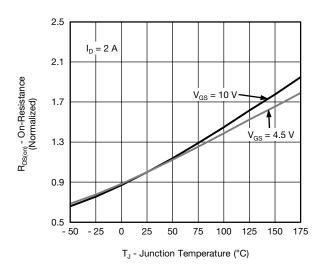
On-Resistance vs. Gate-to-Source Voltage



Gate Charge



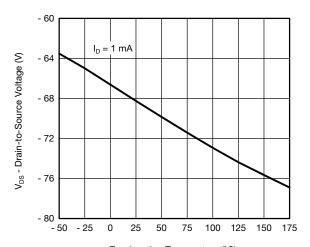
Source Drain Diode Forward Voltage



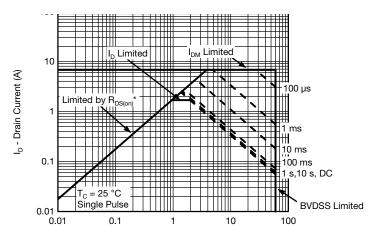
On-Resistance vs. Junction Temperature

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TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



T_J - Junction Temperature (°C) **Drain Source Breakdown vs. Junction Temperature**

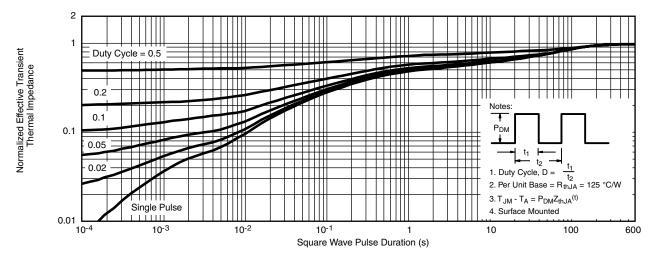


 V_{DS} - Drain-to-Source Voltage (V) * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

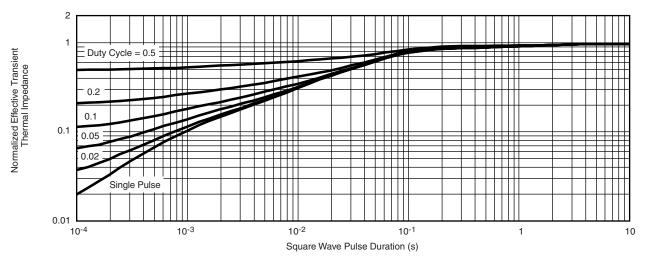
Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?67057.





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SC-70

Ordering codes for the SQ rugged series power MOSFETs in the SC-70 package:

DATASHEET PART NUMBER	OLD ORDERING CODE ^a	NEW ORDERING CODE
SQ1421EDH	-	SQ1421EDH-T1_GE3
SQ1431EH	SQ1431EH-T1-GE3	SQ1431EH-T1_GE3
SQ1440EH	-	SQ1440EH-T1_GE3
SQ1470AEH	-	SQ1470AEH-T1_GE3
SQ1539EH	-	SQ1539EH-T1_GE3
SQ1563AEH	-	SQ1563AEH-T1_GE3
SQ1902AEL	-	SQ1902AEL-T1_GE3
SQ1912AEEH	-	SQ1912AEEH-T1_GE3
SQ1912EH	-	SQ1912EH-T1_GE3

Note

a. Old ordering code is obsolete and no longer valid for new orders





SC-70: 6-LEADS





	MIL	LIMET	ERS	INCHES		
Dim	Min	Nom	Max	Min	Nom	Max
Α	0.90	-	1.10	0.035	_	0.043
A ₁	-	-	0.10	-	-	0.004
A_2	0.80	-	1.00	0.031	-	0.039
b	0.15	-	0.30	0.006	_	0.012
С	0.10	-	0.25	0.004	_	0.010
D	1.80	2.00	2.20	0.071	0.079	0.087
Ε	1.80	2.10	2.40	0.071	0.083	0.094
E ₁	1.15	1.25	1.35	0.045	0.049	0.053
е		0.65BSC			0.026BSC	;
e ₁	1.20	1.30	1.40	0.047	0.051	0.055
L	0.10	0.20	0.30	0.004	0.008	0.012
9	7°Nom				7°Nom	



RECOMMENDED MINIMUM PADS FOR SC-70: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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