Vishay Semiconductors

ROHS COMPLIANT

High Performance Schottky Rectifier, 1 A



Anode

-0

DO-214AC (SMA)

1 A

100 V

0.78 V

1 mA at 125 °C

150 °C

Single die

1.0 mJ

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-10MQ100NPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS VALUES U					
I _{F(AV)}	Rectangular waveform	1	А			
V _{RRM}		100	V			
I _{FSM}	t _p = 5 μs sine	120	А			
V _F	1.5 A _{pk} , T _J = 125 °C	0.68	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10MQ100NPbF	UNITS		
Maximum DC reverse voltage	V _R	100			
Maximum working peak reverse voltage	V _{RWM}	100	v		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 126 °C, On PC board 9 mm ² island (0.013	1.5	•		
See fig. 4		50 % duty cycle at T_L = 135 °C, On PC board 9 mm ² island (0.013	1			
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load	120		
non-repetitive surge current, $T_J = 25 \text{ °C}$ See fig. 6	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	30	A	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 0.5 A, L = 8 mH		1.0	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μs Frequency limited by T_J maximum V_A = 1.5 x V_R typical		0.5	А	

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DO-214AC (SMA)

PRODUCT SUMMARY

Package

 V_R

V_F at I_F

I_{RM}

T_{.1} max.

Diode variation

 E_{AS}





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST C	VALUES	UNITS	
Maximum forward voltage drop See fig. 1		1 A	— T _J = 25 °C	0.78	V
	V (1)	1.5 A	1j=25 C	0.85	
	V _{FM} ⁽¹⁾	1 A	T ₁ = 125 °C	0.63	
		1.5 A	$I_{\rm J} = 125$ C	0.68	
Maximum reverse leakage current See fig. 2	I _{BM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.1	mA
	IRM (17	T _J = 125 °C		1	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J} \text{ maximum} \qquad \qquad$		0.52	V
Forward slope resistance	r _t			mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}$, $T_J = 25 \text{ °C}$, test signal = 1 MHz		38	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 2.0		2.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C	
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	80	°C/W	
Approximate weight			0.07	g	
Approximate weight			0.002	oz.	
Marking device		Case style SMA (similar D-64)	1,	J	

Note

(1)

 $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



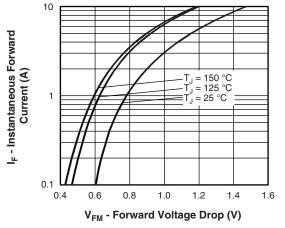


Fig. 1 - Maximum Forward Voltage Drop Characteristics

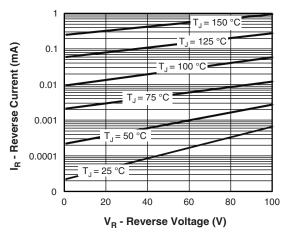


Fig. 2 - Typical Peak Reverse Current vs. Reverse Voltage

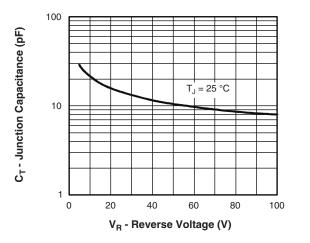
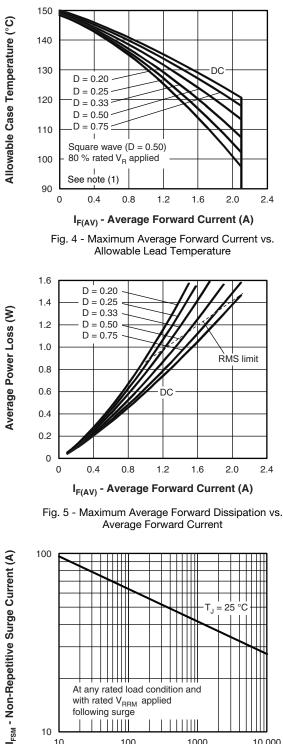


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

VS-10MQ100NPbF

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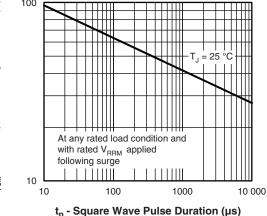


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE

Device code	VS-	10	М	Q	100	N	TR	PbF
	1	2	3	4	5	6	7	8
	 Vishay Semiconductors product Current rating (10 = 1 A) M = SMA Q = Schottky "Q" series 							
	5 - 6 - 7 - 8 -	 N = new SMA TR = tape and reel 						

ORDERING INFORMATION (Example)							
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPT						
VS-10MQ100NTRPbF	5AT	7500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95400			
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			
SPICE model	www.vishay.com/doc?95371			



Outline Dimensions

Vishay Semiconductors

SMA

DIMENSIONS in inches (millimeters)

DO-214AC (SMA)





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